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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10028804	FILING DATE 12/28/2001	CLASS 714	SUBCLASS 38	GAU 2184	EXAMINER Bronscheil
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**APPLICANTS: Miura Takashi; Miyamori Takashi;
RSD

**CONTINUING DATA VERIFIED: BPB

** FOREIGN APPLICATIONS VERIFIED:
JAPAN P2000-402630 12/28/2000 BPB

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no
Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

217806US2

TITLE : Semiconductor integrated circuit, system board and debugging system

U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
ISSUE FEE			Total Claims	Print Claim for O.G.	
Amount Due	Date Paid		DRAWING		
		Primary Examiner	Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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